A Study of Extending Transformation-based Synthesis to Incompletely-specified Functions

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Abstract—Given a completely-specified reversible function, existing transformation-based synthesis (TBS) methods find a reversible circuit that can then be mapped to a quantum circuit. This paper presents a preliminary study of extending the basic TBS method, and a variant of the method that uses Reed-Muller spectra, to integrate the assignment of don't-cares (DC) into the synthesis process, thus extending the TBS methods to handle incompletely-specified reversible functions. This approach is an alternative to assigning DC as a pre-synthesis step. The new methods facilitate the embedding of an irreversible function into a reversible specification thus allowing for the synthesis of a reversible circuit and subsequently a quantum circuit implementing the irreversible function.

Experimental results are given that demonstrate the operation of the proposed methods as well as their limitations. A number of areas are identified for further research.

REFERENCES

- D. M. Miller, D. Maslov, and G. W. Dueck, "A transformation-based algorithm for reversible logic synthesis," in *Proc. IEEE/ACM Design Automation Conference (DAC)*, 2003, pp. 318–323.
 D. Maslov and D. M. Miller, "Reed-Muller spectra based synthesis of
- [2] D. Maslov and D. M. Miller, "Reed-Muller spectra based synthesis of reversible circuits using a quantum cost metric," in *Proc. Reed-Muller Workshop*, 2005.
- [3] D. M. Miller, G. W. Dueck, and R. Wille, "Synthesizing reversible circuits from irreversible specifications using Reed-Muller spectral techniques," in *Proc. Reed-Muller Workshop*, 2009.
- [4] A. Zulehner and R. Wille, "Make it reversible: Efficient embedding of non-reversible functions," in *Design, Automation and Test in Europe Conference (DATE)*, 2017, pp. 458–463.
- [5] M. A. Perkowski, M. Kumar, B. R. Iyer, N. Metzger, and Y. Wang, "Realization of incompletely specified functions in minimized reversible cascades," in *Portland State Univ. Electrical and Computer Engineering Faculty Publications 232*, 2007.
- [6] T. Toffoli, "Reversible computing," Tech memo MIT/LCS/TM-151, MIT Lab for Comp. Sci, 1980.
- [7] M. Z. Rahman and J. E. Rice, "Templates for positive and negative control Toffoli networks," in *Lecture Notes in Computer Science*, vol. 8507, 2014.
- [8] D. M. Miller and G. W. Dueck, "Translation techniques for reversible circuit synthesis with positive and negative controls," in *Recent Findings* in Boolean Techniques: Selected Papers from the 14th International

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Workshop on Boolean Problems, R. Drechsler and D. Große, Eds. Springer International Publishing, 2021, pp. 143–165.

- [9] C. Moraga, "Using negated control signals in quantum computing circuits," *Facta Universitatis(Nis) Ser.: Elec. Energ.*, vol. 24, no. 3, pp. 423–435, 2011.
- [10] M. Soeken and M. K. Thomsen, "White dots do matter: Rewriting reversible logic circuits," in *Lecture Notes in Computer Science*, vol. 7948, 2013.
- [11] A. Peres, "Reversible logic and quantum computers," *Physical Review A*, vol. 32, no. 6, pp. 3266–3276, 1985.
 [12] Z. Sasanian and D. M. Miller, "NCV realization of MCT gates with
- [12] Z. Sasanian and D. M. Miller, "NCV realization of MCT gates with mixed controls," in *Proceedings of 2011 IEEE Pacific Rim Conference* on Communications, Computers and Signal Processing, 2011, pp. 567– 571.
- [13] M. Thornton, D. Houngninou, and D. Miller, "Computing the Reed-Muller spectrum/Algebraic Normal Form: Functional Methods," in Advances in the Boolean Domain, B. Steinbach, Ed. Cambridge Scholars Publishing, 2022, ch. 1, pp. 3–28.
- [14] D. Houngninou, M. Thornton, and D. Miller, "Extracting the Reed-Muller Spectrum/Algebraic Normal Form from a Circuit Specification," in Advances in the Boolean Domain, B. Steinbach, Ed. Cambridge Scholars Publishing, 2022, ch. 2, pp. 33–57.
- [15] D. Maslov, "Reversible logic synthesis," Ph.D. dissertation, University of New Brunswick, 2003.
- [16] D. M. Miller, R. Wille, and Z. Sasanian, "Elementary quantum gate realizations for multiple-control toffoli gates," in *41st IEEE International Symposium on Multiple-Valued Logic*, 2011, pp. 288–293.
- [17] Z. Sansanian and D. M. Miller, "Reversible and quantum circuit optimization: A functional approach," *Lecture Notes in Computer Science*, vol 7581, 2013.
- [18] R. Wille, D. Große, L. Teuber, G. W. Dueck, and R. Drechsler, "RevLib: An online resource for reversible functions and reversible circuits," in *Int'l Symp. on Multi-Valued Logic*, 2008, pp. 220–225, RevLib is available at www.revlib.org.
- [19] D. Maslov, G. W. Dueck, and N. Scott, "Reversible Logic Synthesis Benchmarks Page," 2005, http://webhome.cs.uvic.ca/~dmaslov.
- [20] M. Saeedi, "QDA Reversible Benchmarks," 2008, http://ceit.aut.ac.ir/qda/benchmarks.htm.
- [21] J. Henderson, E. Henderson, A. Sinha, M. Thornton, and D. Miller, "Automated quantum oracle synthesis with a minimal number of qubits," in *SPIE Quantum Information Science, Sensing and Computation XV*, 2023, p. 18.
- [22] K. Smith and M. Thornton, "A quantum computational compiler and design tool for technology-specific targets," in *IEEE International Symposium on Computer Architecture*, 2019, pp. 579–588.